EE 491 WEEKLY REPORT 9   Date: 3/28/2016

Group number: DEC1619
Project title: Drive Circuitry for Ultrasound Brain Simulator
Client &/Advisor: Dr. Bigelow
Team Members/Role: Zechariah Pettit – Team Webmaster, Honghao Liu – Key Concept Holder, Miguel Mondragon – Team Leader and Webmaster

✓ **Weekly Summary (Short summary about what you did this week)**

Had weekly meeting with Dr. Bigelow regarding issues with parts orders, plans for PCB test board, and further discussion regarding project as a whole. Work on PCB test board designs in Multisim began. Individual research and work continued.

✓ **Past week accomplishments (please describe as what was done, by whom, when)**

❖ Group met with Dr. Bigelow at 9AM on Wednesday 3/23/2016 to discuss the matters of ordering parts for testing, plans for a PCB test board for the parts, and some further explanation of portions of the project not covered by our senior design project.

❖ Zechariah met with the parts shop to fix issues with our parts order and completed the order, now waiting for parts.

❖ All group members worked on their individual parts of the project where possible.

❖ Group began work on a PCB test board in Multisim.

✓ **Pending issues (if applicable)**

❖ Work will be slow until the ordered parts can arrive and be tested.

✓ **Individual contributions**

NAME	Individual Contributions	Hours this week	HOURS cumulative
Honghao Liu	Individual Assignment Research, Team Meeting	2	21
Miguel Mondragon	Professor Meeting, Individual Assignment Research, Team Meeting	2	21
Zechariah Pettit	Professor Meeting, Individual Assignment Research, Team Meeting	2	21

✓ **Comments and extended discussion**

None.

✓ **Plan for coming week (please describe as what, who, when)**
  ❖ Meet with Dr. Bigelow at 9AM on Wednesday.
  ❖ Miguel Mondragon will be working on the mutism test board for his parts.
  ❖ Zechariah Pettit will be working on the same.
  ❖ Honghao Liu is familiarizing himself with the necessary tools to program and properly utilize the TI Launchpad which serves as the current plan for the DAC multiplexer portion of the circuit.
  ❖
✓ **Summary of weekly advisor meeting (if applicable/optional)**